APPLE II SERIAL INTERFACE CARD (A2L0008) INSTALLATION AND OPERATING MANUAL

# SERIAL INTERFACE MANUAL INSTALLATION AND OPERATING MANUAL

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## APPLE II SERIAL INTERFACE CARD

## INTRODUCTION

These are the fundamental abilities of the APPLE Serial Interface, using the nearly universal RS232 standard:

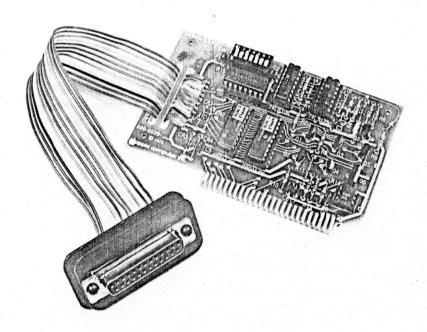
- 1. Output from the APPLE II can be sent to a serial printer or other external serial device, to the APPLE's TV screen, or to both. The Serial Interface can supply the necessary line-feeds with carriage-returns, etc.
- 2. Input for the APPLE II can be taken either from an external device or from the APPLE's keyboard, or from both simultaneously.
- 3. The APPLE II can handle half-duplex communications at rates from 75 to 19,200 baud, in both directions, with a printer, another APPLE, a terminal, modem or other RS232 external device.
- **4.** The Serial Interface can also be connected for current-loop operation with a Teletype.

While this document is intended primarily for APPLE users who are familiar with the RS232 interface, many of the terms and concepts will be explained.

#### I INSTALLATION

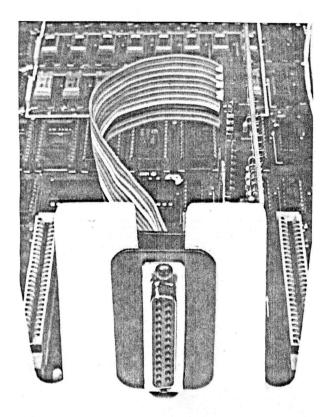
# HOW TO INSTALL THE SERIAL INTERFACE

The Serial Interface consists of three parts: the Interface printed-circuit card itself, a female DB-25 connector, and a flat ribbon cable between them.



To install the Serial Interface, you will simply plug the Interface card into a socket inside the APPLE II, and then tighten a clamp to hold the DB-25 connector in place, as follows:

- 1. Turn off the power switch on the back of the APPLE II. This is important to prevent damage to the computer.
- 2. Remove the cover from the APPLE II. This is done by pulling up on the cover at the rear edge (the edge farthest from the keyboard) until the two corner fasteners pop apart. Do not continue to lift the rear edge, but slide the cover backward until it comes free.
- 3. Inside the APPLE II, across the rear of the circuit board, there is a row of eight long, narrow sockets called "slots." The leftmost one (looking at the computer from the keyboard end) is slot #Ø, and the rightmost one is slot #7. Insert the "fingers" portion of the Serial Interface card into slot #1, the second socket from the left. The "fingers" portion will enter the socket with some friction and will then seat firmly. The Interface card may be placed in any slot except slot #Ø, the leftmost. However, APPLE's stan-



dard location for printer interfaces is slot #1 (the second from the left). This manual and most APPLE software for the Serial Interface are written assuming you have installed the Serial Interface card in slot #1.

- 4. Slip the DB-25 connector and its two metal plates as far down as possible into one of the three long vertical openings in the back of the APPLE II case. One plate goes on the inside of the case; the other plate goes on the outside of the case with the connector's flange on the outside of this plate. Any of the three large vertical openings may be used, but it is customary to use the middle one. Notice that the connector is not symmetrical. When seen from the back of the APPLE II, the longer side of the connector should be on the left (although it will work in either position).
- **5.** Tighten the screws on the DB-25 connector just until the connector assembly can no longer be moved in the opening. Excessive tightening will cause the metal plates to bend.
- **6.** Replace the cover of the APPLE II, remembering to start by sliding the front edge of the cover into position. Press down on the two rear corners until they pop into place.
- 7. The Serial Interface is installed, and the APPLE II may now be turned on.

## COMPATIBILITY WITH EXTERNAL DEVICES

For communications between computers and computer-related equipment, the most widespread and universal standard is the RS232 standard. The RS232 standard specifies the electrical parameters, the form of the signals, and even the type of connector to be used in an interface. The APPLE Serial Interface complies with this standard.

The RS232 standard allows for a number of different communication speeds. These speeds are measured in terms of a unit called the "baud." Each multiple of 10 baud is equal to about 1 character sent or received per second; 300 baud is roughly equal to 30 characters per second. The Serial Interface can operate at any of 256 different speeds, from 75 baud to 19,200 baud.

Computers and their related devices do not actually send the keyboard characters themselves, of course. Each character is encoded in the form of electrical signals, and it is these electrical signals which are sent and received.

The APPLE Serial Interface can communicate with any device that specifies RS232 operation between 75 and 19,200 baud. Many devices can operate at a number of speeds. Very often a set of switches or a rotary dial selects the baud rate. These external devices should be set to a particular baud rate before being connected to the APPLE. The highest baud rate available is usually preferred. The Serial Interface should be set for the same baud rate, using the first 3 levers of the Serial Interface's **DIP** switch (this is explained in the section, SERIAL INTERFACE OPERATING PARAMETERS). All common baud rates are listed in the section, SERIAL INTERFACE TIMING.

While such operation does not conform to the RS232 standard, the APPLE Serial Interface can also be operated in the current-loop mode necessary to communicate with a serial teleprinter such as the Teletype Model 33ASR.

### **RS232 CONNECTOR USAGE**

The standard DB-25 connector, which is supplied with the Serial Interface, has 25 pins. Six of these are connected internally to the APPLE Serial Interface, but for most applications only three of them need be used. If you don't have a ready-made cable that can go from the Interface's DB-25 connector to the external device, then you will have to wire an interconnecting cable. A cable is just a number of electrically distinct wires that physically run alongside each other. When you wire the cable, you will have to refer to the DB-25 connector's pin numbers. These numbers are molded into the connector, although sometimes they are almost vanishingly small.

The following list describes the functions of the active pins on the Serial Interface connector. The other pins may be left unconnected.

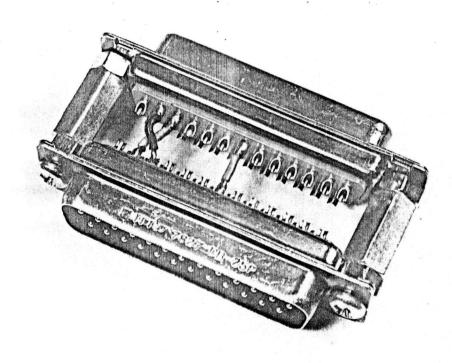
- PINS 4 & 5 These pins have been wired together (jumpered) at the Serial Interface card. No connection need be made to these pins.
- PINS 6, 8 & 20 These pins have been wired together (jumpered) at the Serial Interface card. No connection need be made to these pins.
- PIN 7 This is called "signal ground." It should be wired to pin 7 at the other end of the cable. If there is no connector at the other end of the cable, then the Serial Interface's pin 7 should be wired to a signal ground connection on the external device. (If this is insufficient information, any additional data would have to be supplied by the manufacturer or designer of the external device.)
- PIN 2 The characters from the external device arrive at the computer via this pin.
- PIN 3 The characters leaving the computer, on their way to the external device, exit via this pin.

Pins 2 and 3 have been left for last since, if the external device end of the cable is another 25 pin connector, there are two ways that they might be wired. No damage is caused by wiring these pins the wrong way, but characters will not be sent out or received.

A. If the external device is a terminal or printer with an RS232 interface itself, then pin 2 on the APPLE's end of the cable should be wired to pin 2 at the external device's end of the cable. Similarly, pin 3 on the APPLE's end of the cable should be wired to pin 3 at the external device's end of the cable. Most of these devices, like the APPLE Serial Interface, also have a female DB-25 connector. Therefore your cable will (most likely) need to have a male DB-25 connector at each end.

**B.** If the external device is a modem, or another computer with a standard serial interface, then *its* interface will send characters out via pin 3 and receive characters via pin 2 just as the APPLE Serial Interface does. Therefore, you must wire pin 2 at the APPLE's end of the cable to pin 3 at the modem end of the cable; and wire pin 3 at the APPLE's end of the cable to

pin 2 at the modem end of the cable. Modems usually have a **male** DB-25 connector. Therefore, you will probably need a cable with a **female** DB-25 connector at the modem end, and a **male** DB-25 connector at the APPLE end.



Note that only the wires to pins 2 and 3 are involved. No other wires need be changed, no matter what external RS232 device is connected to the APPLE Serial Interface.

Most commercially prepared cables are simply extension cables: they connect identically numbered pins at the two ends of the cable. For use with a modem, you may have to re-wire the cable, interchanging the wires to pins 2 and 3 at one end. If you would rather not tamper with a ready-made cable, you can make an adapter with a male DB-25 connector at one end and a female DB-25 connector at the other end. It should be wired with pins 7 connected, and pins 2 and 3 cross-connected. This adapter, when connected between the APPLE's Serial Interface and a standard RS232 cable, allows the Interface to "talk" to most modems.

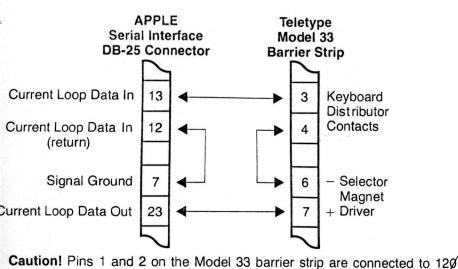
#### CURRENT-LOOP OPERATION WITH A TELETYPE

If you wish to use the APPLE Serial Interface to communicate with a currentloop teleprinter such as the Teletype Model 33ASR, you will be interested in three other Interface pins.

PIN 13 In current-loop operation, the characters from the Teletype arrive at the APPLE via this pin. This pin should be wired to terminal 3 on the Teletype Model 33's barrier strip.

PIN 12 The return path of the input current loop (the loop for characters arriving from the Teletype) should be connected to this pin. (In fact, the Serial Interface does not care which way current flows through this input loop. We have arbitrarily chosen pin 13 as the input and pin 12 as the return, but the roles of these two pins can be interchanged.) We suggest using signal ground (at pin 7) for the return path, in which case you should connect pin 12 to pin 7.

PIN 23 In current-loop operation, the characters leaving the APPLE, on their way to the Teletype, exit via this pin. Wire this pin to terminal 7 on the Teletype Model 33's barrier strip. The return path for this output current loop is also the signal ground at pin 7. For half-duplex operation, connect terminals 4 and 6 on the Teletype Model 33's barrier strip.



#### II OPERATION

#### USING THE APPLE SERIAL INTERFACE

The Serial Interface allows the APPLE II to communicate with other electronic devices which are external to the computer. These devices may be —to give a few examples—terminals, printers, or other computers. The Serial Interface can be controlled through BASIC programs or through assembly-language programs. It can also be controlled directly, by typing a few characters on the APPLE's keyboard.

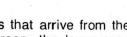
In the following discussion, it will be assumed that you are familiar with the APPLE II BASIC Programming Manual, and that your APPLE II is operating in BASIC, with the Serial Interface installed in slot #1.

Here is a list of the most common tasks the Serial Interface is called upon to do, and the commands that accomplish them.

- 1. Send subsequent output to the Serial Interface. PR#1
- 2. Cancel the effect of PR#1, sending output only to APPLE's TV screen. PR#Ø
- 3. Accept subsequent input from the Serial Interface, as well as from the APPLE's keyboard. IN#1
- 4. Force the APPLE to convert all lower-case characters to upper-case, as they arrive from the external device.



(type the Be key, then type key)



5. Allow the APPLE to accept lower-case characters that arrive from the external device. If displayed on APPLE's TV screen, the lower-case characters will appear as upper-case characters in inverse video.



These tasks are more fully explained, and some fine points considered, in the next few pages.

To understand exactly how the Serial Interface operates, it is useful to think of the APPLE II as divided into three parts:

- 1. The APPLE's keyboard, which generates characters (when you type on it).
- 2. The APPLE's TV screen, which can absorb characters (and make them visible).
- 3. The APPLE's processor, or "brain," which can control the flow of characters, and act upon them.

Volts AC.

You can also think of any external device as being able to **generate** characters or **absorb** characters, or both. The external device may or may not have a "brain," but this is not important in understanding the operation of the Serial Interface.

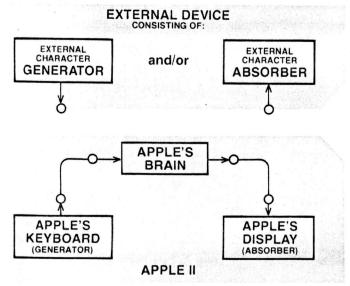


FIGURE 1

Figure 1 shows the three parts of the APPLE II, and the only parts of any external device that affect the Serial Interface. It also shows the normal interconnection between these parts. When the APPLE II is first turned on, it will ignore the external world, listening only to its own keyboard and displaying characters only on its own TV screen.

# PRELIMINARY DISCUSSION OF THE IN# AND PR# COMMANDS

There are eight sockets, called "slots," on the back of the main circuit board inside the APPLE II. The leftmost one (as viewed from the keyboard end of the computer) is slot #Ø, and the rightmost one is slot #7. (See the section, HOW TO INSTALL THE SERIAL INTERFACE.) APPLE BASIC has two commands for selecting among these slots for input and output. In effect, when you first invoke BASIC, the commands

IN#Ø

and

are automatically executed. The first of these commands,  $IN\#\emptyset$ , tells the APPLE to

Take **IN**put from the APPLE keyboard.

And the second command, **PR#Ø**, instructs the APPLE to

Send PRinting to the APPLE's TV screen.

This is the "normal," or APPLE-alone condition shown in Figure 1. Now, however, if the command (or program statement)

#### IN#1

is executed, the APPLE will henceforth take its input from whatever is plugged into slot #1. Similarly, if the command

#### **PR#1**

is executed, all output will be sent to whatever is plugged into slot #1. If there is nothing plugged into the specified slot, then the system may hang, or your program may be erased, or other strange behavior may result. Notice that slot #9 is special, and refers to the APPLE itself.

# SENDING YOUR OUTPUT TO AN EXTERNAL DEVICE AND RECEIVING INPUT FROM AN EXTERNAL DEVICE

In the following examples, the commands

PR#1

and

IN#1

will be typed on the APPLE's keyboard. If you have put your Serial Interface into slot #1 (the second one from the left, as described in the section, HOW TO INSTALL THE SERIAL INTERFACE) the commands will work exactly as shown. If you use some other slot, you will have to substitute the number of that slot. Slot #Ø may not be used for the Serial Interface.

Attach an appropriate cable (see the section, RS232 CONNECTOR USAGE) from the DB-25 connector of the Serial Interface to the external device with which you wish to communicate. Reset your APPLE II by pressing the

CTRL CTRL

#### **PR#1**

( and press the RETURN key, of course). From now on, any characters you type will be sent out through the Serial Interface to the external device. The characters will appear on the APPLE's TV screen only if levers 5 and 6 of the Serial Interface's DIP switch (located directly on the Interface card itself, near its upper edge) are ON when PR#1 is typed. (This is more fully explained in the section, SERIAL INTERFACE OPERATING PARAMETERS.) Characters coming in from the external device will be ignored. The operation of the system after you type PR#1 is shown in Figure 2.

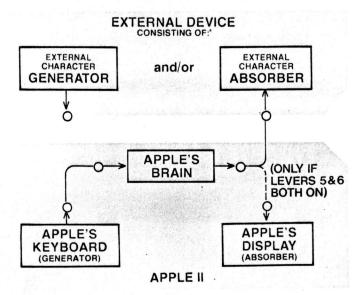


FIGURE 2

Operation of the system after executing PR#1

The APPLE's "brain" is still connected, and the command

#### PR#Ø

will restore normal (Figure 1) operation, in which the APPLE's output characters are not sent through the Serial Interface. Normal operation can also be

#### CTRL

restored by pressing the restored by pressing the restored by pressing the restored by a program key and then typing a G , but this option is not available if the Interface is being controlled by a program.

To let the external source of characters control the APPLE II, use the command

#### IN#1

After this command, the APPLE will accept input from the external device connected to the Serial Interface, as well as from its own keyboard. Figure 3 shows this condition. If there is no external device connected to the Serial

CTRI

Interface, the system will "hang" after this command. Use RESET C to recover.

# EXTERNAL DEVICE CONSISTING OF:

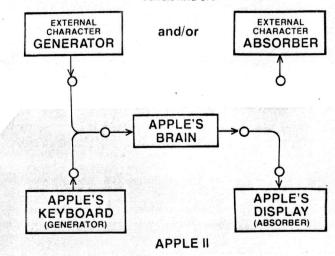


FIGURE 3

Operation of the system after executing IN#1

Normal operation is restored if the command

#### N#Ø

is typed on the APPLE's keyboard. Normal operation is also restored if the external device sends the command

IN#Ø

#### CTRL

Pressing the REET key and typing a on the APPLE's keyboard will also restore normal operation; but this cannot be done from a program, and will not be mentioned again.

Typing both commands,

**PR#1** 

and

#### IN#1

will give the external device full control of the APPLE II. In this "remote mode" (shown in Figure 4), a friend could use your APPLE from across the country—or across the room.

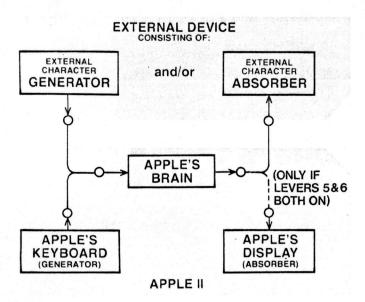


FIGURE 4

Remote Mode: operation of the system after executing PR#1 and IN#1.

#### III DEFAULT PARAMETERS AND THE DIP SWITCH

## INITIALIZING THE SERIAL INTERFACE

Before the Serial Interface can be used, it must be **initialized**. Initializing the Interface sets all of the Interface operating parameters to their **default** values. Assuming slot #1, the Interface is initialized each time either of the following BASIC commands is typed:

PR#1 RETURN or IN#1. RETURN

and each time any of the following Monitor commands are typed:



When used within a *program*, a command (such as PR#1) that transfers APPLE's output to the Serial Interface does *not* initialize the Interface until the first character is actually sent out (with a PRINT statement, for instance). Similarly, if during a *program* a command (such as IN#1) tells the APPLE to get its input from the Serial Interface, the Interface is *not* initialized until the APPLE actually looks for its first input character (in an INPUT statement, for instance).

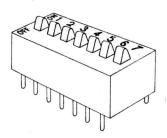
# SERIAL INTERFACE OPERATING PARAMETERS

The Serial Interface has ten user-definable operating parameters. Each time the Serial Interface is initialized, the ten operating parameters are given their **default** values. Five of the default values are determined by the 7 levers of the Serial Interface's **DIP** switch (located on the Interface's printed-circuit card, near the upper edge). The **DIP** switch levers set the default values for these five operating parameters: **Baud Rate** (levers 1, 2 and 3), **Carriage Return Delay** (lever 4), **Line Width** plus **APPLE Video**(levers 5 and 6), and **Line Feed** (lever 7). Changing the settings of the **DIP** switch levers after initialization has no effect until the *next* initialization.

## SETTING THE DIP SWITCH DEFAULTS

#### 1) Baud Rate

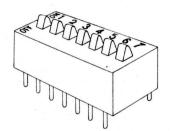
	DIF	•		Defau	ult	
Sw	itch L	.evers	3	Baud F		
1	2	3				
On	On	On	===	11Ø	baud	
Off	On	On		134.5	baud	
On	Off	On	-	3ØØ	baud	
Off	Off	On	==	12ØØ	baud	
On	On	Off		24ØØ	baud	
Off	On	Off	-	4800	baud	
On	Off	Off	=	96ØØ	baud	
Off	Off	Off	=	19200	baud	



On initialization, the settings of **DIP** switch levers 1, 2 and 3 determine the rate at which bits may be transmitted to the external device. 300 badd is 300 bits per second. Under default conditions, each character is transmitted using 11 bits (1 start bit, 8 data bits, and 2 stop bits).

### 2) Carriage Return Delay

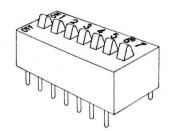
DIP		Default	
Switch		Car. Return	
Lever 4		Delay	
On		Disabled	
Off	rivals.	Enabled	



If **DIP** switch lever 4 is Off (Delay Enabled), the Serial Interface will wait briefly (approximately ¼ second) after transmitting a carriage return, to allow the printer to complete this movement. If you are transmitting to an external TV screen, this delay is probably unnecessary, and lever 4 may be turned On (Delay Disabled).

### 3) Line Width plus APPLE Video

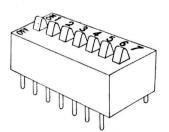
DIP	Default	Default
Switch	Line	APPLE
Levers	Width	Video
5 6		
On On =	4Ø Char/Line	Enabled
Off On =	72 Char/Line	Disabled
On Off =	8Ø Char/Line	Disabled
Off Off =	132 Char/Line	Disabled



After a carriage return, the settings of **DIP** switch levers 5 and 6 determine the maximum number of characters transmitted before the Serial Interface will force another carriage return to be sent out. Characters will be displayed on the APPLE's TV screen only if the default line width is set to 4Ø characters per line (levers 5 and 6 On). After initialization, the line width can be changed from 4Ø characters per line, but the display on the APPLE's TV screen will not correspond to the display on the external device, as transmitted carriage returns are not accompanied on the APPLE's TV screen by line feeds.

#### 4) Line Feed

Default
Line
Feed
<ul><li>Disabled</li></ul>
= Enabled



If **DIP** switch lever seven is Off (Line Feed Enabled), the Serial Interface will transmit a line feed after each carriage return it transmits. If the external device automatically supplies its own line feed after each carriage return received, you can set lever seven to On (Line Feed Disabled) to avoid double-spacing.

#### **PERMANENT DEFAULTS**

During each initialization, the five remaining operating parameters are set to their **permanent** default values:

- 1. Parity defaults to its disabled condition (no parity bit).
- 2. Checksum defaults to its disabled condition (no checksum character).
- 3. Lower-Case defaults to its disabled condition (converts all incoming lower-case characters to upper-case).
- 4. Number of Data Bits defaults to 9 (8 data bits plus one start bit).
- 5. Number of Stop Bits defaults to 2.

These parameters will be explained in the following section; they can only be changed by software commands *after* initialization.

### IV ACCESS TO OPERATION PARAMETERS

#### DESCRIPTION OF SERIAL INTERFACE OPERATION

For most applications, the default operating parameters (both those that are fixed and those that can be set with the **DIP** switch) will be just what you need: your parameters will be set each time the Interface is initialized. In that case, the following section will be interesting but not necessary. However, the Serial Interface is designed to be very flexible, so that its operating parameters can be easily modified for use in a wide variety of special applications. This section gives a rather detailed description of the Serial Interface's operation. The following section shows you how to make any of the many possible modifications to that operation, should they be necessary.

Each character that is sent out through the Serial Interface is transmitted as a series of bits, in the following sequence:

- 1. One "Start Bit," a "low" voltage which tells the external device that a character is going to be transmitted.
- 2. From two to eight "Data Bits" (default is eight bits; can be changed by the user), a sequence of "high" and "low" voltages that represent the actual character code being transmitted. The default is eight bits because the APPLE normally handles data in eight-bit groups. If your external device sends and receives data in groups of fewer than eight bits, you must set the Serial Interface to send and receive these smaller groups.
- 3. If enabled, one "Parity Bit" (default is no parity bit; even or odd parity can be enabled by the user). This is a transmission-accuracy checking bit which the external device looks at for errors and then discards.

The parity bit is found as follows: all the 1-bits in the actual character code are added together, and the result's evenness or oddness is compared with the type of parity-checking selected. For instance, the binary ASCII code for the letter S is 1010011; the sum of the 1-bits is 4, an even number. If Even parity has been enabled, the comparison is true, and a 0-bit is sent at the end of the character. If Odd parity has been enabled, the comparison is false, and a 1-bit is sent. If parity has not been enabled, no extra bit is sent. Check your device's operation manual to see if it sends and receives parity bits.

4. From one to 127 "Stop Bits" (default is two bits; can be changed by the user), a "high" voltage which tells the external device that a character has been completely transmitted. Each external device requires a particular number of stop bits after every character; see the device's operation manual.

This same sequence will be used by the external device when it transmits to the APPLE. Timing is very important to correct transmission and reception. The Serial Interface sends out and receives bits at fixed intervals of time set by the "Baud Rate" (default is set by **DIP** switch levers 1, 2 and 3; can be

changed by the user). The Serial Interface and the external device *must* be set to the same baud rate and parity option, in order to interpret the sequence of "high" and "low" voltages correctly. The same is true when the external device is transmitting characters to the APPLE Serial Interface.

When the Serial Interface has sent out the number of characters set by the "Line Width" (default is set by **DIP** levers 5 and 6; can be changed by the user), it transmits a "Carriage Return" to the external device. After sending a carriage return, it may wait during a fixed ¼-second "Carriage Return Delay" (if enabled: default set by DIP lever 4; can be changed by the user) before sending the next character, to allow the printer to complete this movement. Then the Interface may send a "Line Feed" (if enabled: default set by **DIP** lever 7; can be changed by the user), so that subsequent characters will appear on the following line.

Finally, each time it completes sending 256 characters in a "Batch Move," the Serial Interface may send a "Checksum" character (default is no checksum character; can be changed by the user). This is a transmission-accuracy checking character which the external device looks at for errors and then discards. The checksum character is found by XORing the previous 256 characters, as follows: the second character is XORed with the first, the third with that result, the fourth with that result, and so on. Check your device's operation manual to see if it sends and receives checksum characters. During Batch Moves, the Serial Interface and the external device must be set to the same checksum option.

### LOWER-CASE CHARACTERS

While the APPLE generates and displays characters only in upper case capital letters), your external device may generate both upper-case and ower-case characters. When lower-case characters are received by the APPLE through the Serial Interface, they can be treated in two different vays:

- Convert all incoming lower-case characters to upper-case characters (incoming upper-case characters are not affected). Since this is the usual APPLE mode, any incoming characters displayed on the APPLE's TV screen will look fine. This is the permanent default condition; but it can be changed by the user, after initialization.
- Accept all incoming lower-case characters as lower-case (again, incoming upper-case characters are not affected). APPLE's TV screen display of characters is designed for upper-case only, so any display of incoming lower-case characters will look strange. If the characters are being displayed as they arrive from the external device, APPLE will show the lower-case characters as upper-case characters in inverse video (black letters on a white background). Once stored in APPLE's memory, lower-

case characters will be displayed (when LISTed, for instance) as a strange assortment of upper-case characters in normal (white on black) video. There is one exception to this: if the stored lower-case characters are being displayed as they are sent out through the Serial Interface, they will again appear as upper-case characters in inverse video. Note that these peculiar *displays* do not reflect the lower-case characters themselves: in this mode they are *stored* correctly in APPLE's memory, and may be printed correctly on any appropriate external device. Note also that this mode does not add any capability to *generate* lower-case characters from the APPLE keyboard.

# CHANGING SERIAL INTERFACE PARAMETERS THROUGH SOFTWARE COMMANDS

Ten of the Serial Interface parameters can be changed from their initialized (default) values, through the use of commands in machine language or BASIC. Once an Interface parameter is set by a software command, that parameter remains unchanged until the Serial Interface is reinitialized or the parameter is reset by another software command. For more discussion of the various parameters functions, see the previous section.

In the following descriptions, the letter "s" refers to the number of the printed-circuit board slot inside the APPLE, in which the Serial Interface card is installed (see the section, HOW TO INSTALL THE SERIAL INTERFACE).

## 1. BAUD RATE (assembly-listing variable: BRATE)

Memory location 1144+s (\$478+s, in Hexadecimal) contains APPLE's baud "quantum" number, which specifies how many "quanta" the APPLE is to wait between sending out bits through the Serial Interface. One quantum equals 53 APPLE II cycles (51.94 microseconds) per transmitted bit. The default value is set with levers 1, 2 and 3 of the Interface card's DIP switch (see the section, SETTING THE DIP SWITCH DEFAULTS). From BASIC, to change the baud rate from the default value to B use the command

POKE 1144+s, r

where r is the integer, from  $\emptyset$  through 255, that is closest to  $1/(.\emptyset \emptyset \emptyset \emptyset 5194 * B)$ 

For further information, see the section SERIAL CARD TIMING.

# 2. STOP BITS (assembly-listing variable: STBITS)

Memory location 1272+s (\$4F8+s, in hexadecimal) contains the Number of Stop Bits (Note: the one parity bit is *included* in this number, if

parity is enabled). The default value is 2 stop bits (and no parity bit). To change the number of Stop Bits from BASIC, use the command POKE 1272+s, r

where r is an integer, from1 through 127. To determine the correct number of stop bits for your external device, see the external device's operation manual.

Note: you must add the one parity bit to the Number of Stop Bits, if parity is enabled.

### 3. PARITY/CHECKSUM OPTIONS (assembly-listing variable: STATUS)

Memory location 1400+s (\$578+s, in hexadecimal) contains a number, the lower three bits of which determine two parity options (enable/disable and even/odd) and one checksum option (enable/disable). If the remote device with which your Serial Interface is communicating requires a parity bit to be sent or received with each character, you can tell your Serial Interface to do this task. You can also specify which type of parity check (even parity or odd parity) is to be sent and received. If your remote device requires that a checksum be sent after every 256 characters in a Batch move, you can tell the Serial Interface to send one. To decide whether your external device requires either a parity bit or a checksum character (or both), consult the device's operation manual. The three Parity/Checksum options are changeable from BASIC by using the command

POKE 1400+s, r

where r is an integer from  $\emptyset$  through 7. The actual value that r should be assigned is determined as follows:

Bit  $\emptyset$ : 1 = odd parity

(This is the least significant,

 $\emptyset$  = even parity

or rightmost, bit.)

Bit 1: 1 = no parity

(initial default value)

 $\emptyset$  = parity enabled

Bit 2: 1 = no checksum

(initial default value)

 $\emptyset$  = checksum enable

First determine whether or not a parity bit need be sent (Bit 1). If yes, then decide whether the parity should be odd or even (Bit Ø). Also, determine whether or not a checksum character need be sent during Batch moves (Bit 2). For example, let's assume that an even parity bit must be sent, with no checksum. Bit Ø gets a value of Ø, Bit 1 gets a value of Ø, and Bit 2 gets a value 1. This binary number 1ØØ is converted to its decimal equivalent of 4 and POKE'ed (assuming slot #1):

4. INPUT/OUTPUT BUFFER (assembly-listing variable: BYTE)

Memory location 1656+s (\$678+s, in hexadecimal) is the input buffer for the individual character that has just been received through the Serial Interface from the external device. Assuming the Interface is in slot #1, the BASIC command

PRINT PEEK (1657)

will print on the APPLE's TV screen the ASCII value of the character just received.

### 5. LINE WIDTH (assembly-listing variable: PWDTH)

Memory location 1784+s (\$6F8+s, in hexadecimal) contains the "Printer Width," or number of characters per line. After transmitting this number of characters, the Serial Interface will then transmit its Carriage-Return sequence. To change the number of characters per line, from BASIC, use the command

POKE 1784+s, r

where r is an integer, from  $\emptyset$  through 255, specifying the number of characters per output line. To determine the maximum line width for your external device, consult the device's operation manual.

Note: if r is set to zero, the Serial Interface will not force any carriage returns to be transmitted. The output characters will be transmitted in a continuous stream.

## 6. DATA BITS (assembly-listing variable: NBITS)

Memory location 1912+s (\$778+s, in hexadecimal) contains the number of Data Bits, plus one for the start bit. In the APPLE, data is handled in groups containing eight bits. If you are communicating with an external device which *also* handles data in eight-bit groups, the default Number of Data Bits is perfect (8 data bits plus 1 start bit). However, if your external device handles data in groups of *fewer* than eight bits, you must set the Serial Interface to send and receive these smaller data groups.

When receiving data groups of fewer than eight bits, the Serial Interface will supply 1's to fill the remaining high-order bits of each eight-bit group in APPLE's memory. Similarly, when the Serial Interface is transmitting data groups of fewer than eight bits, the unused high-order bits in each of the APPLE's eight-bit data groups must be set to 1's.

To change the Number of Data Bits from BASIC, use POKE 1912+s. r

where r is an integer, from 3 (2 data bits plus one start bit) through 9 (8 data bits plus one start bit).

Note: to calculate r, you must add one start bit to the number of data bits. If r is set to less than the default value of 9 (8 data bits plus one start bit), you must set the unused high-order data bits to ones before transmitting the data. Received data will also have unused high-order data bits set to ones.

Example: Binary Coded Decimal is a code for sending numbers in four-bit data groups. The BCD code for the number 7 is 0111. If the Number of Data Bits, r, is set to 5 (4 data bits plus 1 start bit), BCD for the number 7 must be stored in the APPLE's eight-bit byte as 1111Ø111 before the data group Ø111 can be transmitted. Similarly, if the data group Ø111 is received by the Serial Interface, it will be stored in the APPLE's eight-bit byte as 1111Ø111.

# OPERATION MODES (assembly-listing variable: FLAGS)

Memory location 2Ø4Ø+s (\$7F8+s, in hexadecimal) contains a number, four of whose bits determine four separate modes of operation. To alter the operation modes from BASIC, use the command POKE 2040+s, r

where the value of r is determined by use of the following table:

r's Binary Bit Bit#Ø	(Decimal Equiv., If Bit=1) (1)	Operation Set By Bit Value  1 = Line feed after carriage return Ø = No line feed	<b>Default</b> (Set by Lever 7)
Bit#5	(32)	1 = Lower-case input enable Ø = Convert lower-case to Upper-case	(Permanent Default)
lit#6	(64)	<ul> <li>1 = No delay after carriage return</li> <li>Ø = Carriage return delay enable</li> </ul>	(Set by Lever 7)
it#7	(128)	1 = No display on APPLE's TV $\emptyset$ = APPLE's display enabled	(Set by Levers 5&6)*

\*APPLE's TV display is only enabled during initialization if DIP switch levers 5 and 6 are both On.

For example, let us assume that you wish to have line feeds, uppercase only, carriage return delay, and no APPLE display. This would require that bits Ø and 7 have a value of 1, and bits 5 and 6 have a value of  $\emptyset$ . Add up the decimal equivalents of all of the bits that were assigned the value 1 (the decimal equivalents are the numbers in parentheses, next to

the Bit #'s). The decimal equivalents for bits Ø and 7 are 1 and 128 respectively; therefore the total of the decimal equivalents is 129. This value is assigned to r, and POKE'ed (assuming slot #1):

POKE 2041, 129

If you wish to change only Bit #5 (lower-case input enable/convert), you can do so with the following commands:









This changes r's Bit #5 to a zero, the default value. After this command, all lower-case characters arriving through the Serial Interface from an external device will be converted to upper-case characters. Incoming upper-case characters are not affected. This is the APPLE's usual mode, so any APPLE display will look fine.





This changes r's Bit#5 to a one. After this command, lower-case characters arriving through the Serial Interface from an external device will be stored as lower-case characters in APPLE's memory. Upper-case characters are not affected. Since the APPLE was designed for uppercase characters only (BASIC will accept lower-case characters only in quoted strings), any APPLE display of these lower-case characters will look strange on the TV screen. See the previous section for details. However, the characters are stored correctly, and may be printed correctly on any appropriate external device.

Note: the commands [SC ] and [SC ] are Serial Interface input commands. They will have no effect unless the Interface has been initialized for input (by IN#1, for instance).

#### 8. TAB

The TAB and comma functions in Integer BASIC (HTAB in APPLESOFT) will sometimes work in conjunction with the Serial Interface, but have several restrictions (fewest for comma-tabbing). A TAB of less than 18, if it would end directly on a character already printed, may be simply tabbed from that character's position. No TAB can cause printing to occur to the left of the last printed character on the current line. An attempt to do so usually causes printing to occur in the first available position to the right of the last printed character. Both Integer BASIC's TAB and APPLESOFT's HTAB send out a carriage return for every 40 positions in the tab instruction, and then tab the remaining positions. For tabbing to any position (including those beyond position 40), you can use the BASIC command

POKE 36, r

where r is an integer, from Ø through 255, equal to the number of print

positions to be tabbed. This command suffers most of TAB's restrictions, except for the 4Ø-position limit. In APPLESOFT, the TAB function (used inside a PRINT statement) can also cause tabbing of more than 4Ø positions.

## V DIRECT USE OF THE INTERFACE

# TRANSMITTING A CHARACTER WITHOUT USING PR#1

Occasionally, it is useful to send a character out through the Interface without using a PR# command to change the "output vector" (the system pointer that tells your APPLE where to send its output, normally to its TV screen). To use the Serial Interface directly, follow these two steps:

- 1. Into APPLE's accumulator, put the ASCII code of the character to be sent.
- 2. CALL 16384 + (256 \* s)

where s is the Interface's slot number (the equivalent hexadecimal location to CALL is \$CsØØ). There are various ways to get a number into the APPLE's accumulator, but one way is to write a very small machine-language subroutine to do it, and then CALL that subroutine from your BASIC program. To begin, press the RESET key to enter the Monitor (prompt character: \*), and then (assuming slot #1) type

#### 300: A9 11 4C 00 C1 RETURN

Check your work by typing 300L

Ignoring most of the resulting display, the first two lines should look like this:

Ø3ØØ- A9 11 LDA #\$11 Ø3Ø2- 4C ØØ C1 JMP \$C1ØØ

The first instruction (at hexadecimal location \$300) tells the APPLE to LoaD the Accumulator with the number in the next location (hexadecimal location \$301). For now, that number is \$11. The second instruction is equivalent to CALL -16128 in BASIC: it tells the APPLE to JuMP to hexadecimal location \$C100, which starts the Serial Interface character output routine. To use this subroutine in your BASIC program, you must first put into hexadecimal location \$301 (that's location 769, in decimal) the ASCII code for the character you want the Serial Interface to send out. Then you will CALL the subroutine at hexadecimal location \$300 (768, in decimal). Here is a short program that uses the above machine-language routine to send out one character at a time:

1Ø INPUT "LETTER?", L\$ 2Ø POKE 769, ASC(L\$) 3Ø CALL 768 4Ø GOTO 1Ø

### **BATCH MOVES**

At times it is useful to send or receive large amounts of information very quickly. This can be accomplished through use of a *Batch Move*. The Batch subroutines are "utility" routines. They are intended to be used for special

applications such as Data Collection, Mass Storage and Retrieval Systems, and sending program sequences to control external devices. To understand how to use the Batch routines on the simplest level, refer to the examples below. For an example of using the Batch Moves from BASIC, see the next section, BATCH MOVES FROM BASIC.

Note: before using the Batch routines, you must have initialized the Serial Interface (by typing PR#1, for instance) and you must have set the desired parity and checksum parameters. The Batch Move commands deal directly with the Serial Interface (not through the input and output "vectors" set by IN#and PR#); therefore these commands are the same, regardless of which slot contains the Interface card.

#### 1. Batch Output

When in the Monitor (prompt character: \*), type

3F8: 4C 41 C9 RETURN

This prepares the APPLE to jump to hexadecimal location \$C941 in the

CTRL

Serial Interface's Read-Only Memory when a w is typed on the keyboard. This jump causes the Batch Output routine to execute. When you are ready to actually send the data, type

CTRL

addr1 . addr2 Y RETURN

where "addrl" is the hexadecimal starting address of the data, and "addr2" is the hexadecimal ending address of the data. For example, if we wanted to send the information that is stored in memory from address \$2000 through address \$3FFF, we would type

CTRL

2000.3FFF Y RETURN

As soon as the return is typed, the data from address 2000 to address 3FFF will be sent through the Serial Interface from the APPLE to the external receiving device.

### 2. Batch Input

When in the Monitor, type

3F8: 4C 3D C9 RETURN

This prepares the APPLE to jump to hexadecimal location \$C93D in the Serial Interface's Read-Only Memory when a control Y is typed on the keyboard. This jump causes the Batch Input routine to execute. When you are ready to actually receive the data, type

CTRL

addr1.addr2 Y RETURN

where "addrl" is the hexadecimal starting address in which the incoming data will be stored, and "addr2" is the hexadecimal ending address in which the incoming data will be stored. For example, if we wanted to receive data from an external device, and store it in our APPLE's memory from address \$4000 through address \$5FFF, we would type

CTRL

4000.5FFF Y RETURN

As soon as return is typed, the serial data can be sent by the external transmitting device to the Serial Interface. As it is received, the incoming data will be stored in your APPLE's memory from address 4000 through address 5FFF.

Note: when the Serial Interface is instructed to receive a batch move, the cursor on the receiving APPLE's TV screen disappears, and the Interface waits patiently until all the specified locations have been filled with received data. Then the cursor returns.

## **BATCH MOVES FROM BASIC**

While it is easiest to use the Batch routines from the Monitor, it is also possible to do Batch Moves from BASIC. In the following discussion these definitions will hold:

BAL = Beginning Address Low (the two rightmost digits of the 4-digit hexadecimal starting address for the move, converted to decimal)

BAH = Beginning Address High (the two leftmost digits of the 4-digit hexadecimal starting address for the move, converted to decimal)

EAL = Ending Address Low (the two rightmost digits of the 4-digit hexadecimal ending address for the move, converted to decimal)

EAH = Ending Address High
(the two leftmost digits of the 4-digit hexadecimal ending address for the move, converted to decimal)

Suppose you wish to send someone a picture from your APPLE's high-resolution screen (page 1). The memory for this screen lies between 8K and 16K, from hexadecimal address \$2000 to hexadecimal address \$4000. For the Beginning Address \$2000:

BAL =  $\$\emptyset\emptyset$  (hex) =  $\emptyset$  (decimal) BAH =  $\$2\emptyset$  (hex) = 32 (decimal) For the Ending Address  $\$4\emptyset\emptyset\emptyset$ : EAL =  $\$\emptyset\emptyset$  (hex) =  $\emptyset$  (decimal)

EAH = \$40 (hex) = 64 (decimal)

## 1. Batch Output from BASIC

The following BASIC program does the same task that the Monitor Batch Output command did. See the discussion of the Monitor Batch Output for more details.

10 PR#1: PRINT " (initializes Interface)
20 POKE 60, BAL: POKE 61, BAH (sets starting address)
30 POKE 62, EAL: POKE 63, EAH (sets ending address)
40 CALL -14015 (jumps to Output Routine at \$C941)
50 PR#0 (returns to normal TV output)

#### 2. Batch Input from BASIC

In the Batch Output program, above, change line 4Ø to 4Ø CALL -14Ø19 (jumps to Input Routine at \$C93D)

The resulting BASIC program does the same task that the Monitor Batch Input command did. See the Monitor Batch Input discussion for more details. Note that the IN#1 is *not* necessary for accepting input through the Serial Interface, because CALL -14Ø19 deals directly with the Interface (not through the input and output "vectors" set by PR#and IN#). In fact the PR#1 in line 1Ø was necessary *only* to initialize the Interface;

1Ø CALL - 16128 would have done as well.

# VI APPENDIX: SERIAL INTERFACE TIMING

# TABLE OF BAUD RATE QUANTUM NUMBERS

The following is a table that gives seventeen of the most commonly used **baud** rates, along with their quantum value (for POKEing) and percent error. Although only seventeen different **baud** rates are shown here, any integer from Ø through 255 may be POKE'd into the proper address (1144+s), and each will give a different **baud** rate.

Average APPLE II Frequency

1.Ø2Ø4842 MHz

Period

.979926 microseconds

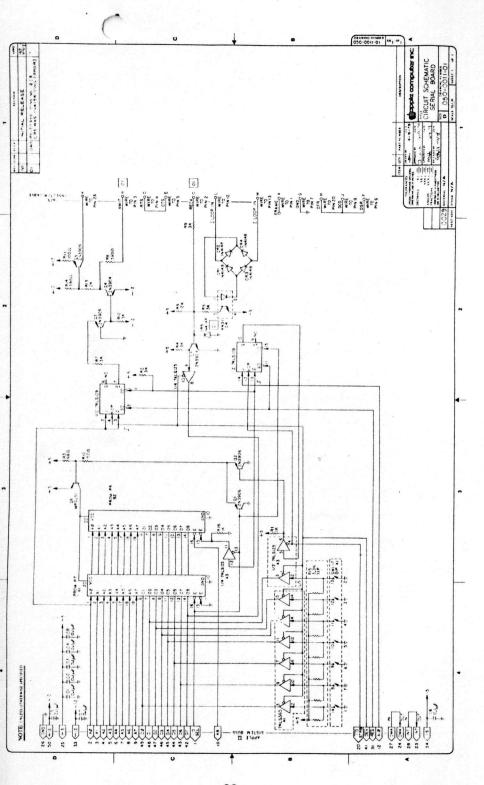
litter +139.7 nanoseconds every 65th cycle

Baud Rate Loop Quantum\*\* 53 APPLE II Cycles (51.94 microseconds)

		Qua	ntur	n No.	Period	Act	ual		
Baud Rate	(H	ex)		(Dec)	(microsec.)	Baud		%	Error
75	\$	ØØ	* *	* Ø	13296		5.2Ø	+	.28
9Ø	\$	D6		214	11115	89	9.96	_	.ø37
11Ø*	\$	BØ		176	9141	109	9.4		.548
134.5*	\$	9Ø		144	7479		3.7	-	.586
15Ø	\$	8Ø .		128	6648	15	Ø.4	+	.28
24Ø	\$	5Ø		8Ø	4155	17.00	Ø.67	+	.28
3ØØ*	\$	4Ø		64	3324	зǿ	<b>2.85</b>	+	.28
45Ø	\$	2B		43	2234		7.74	_	.51
6ØØ	\$	2Ø		32	1662	6Ø	1.7	+	.28
9ØØ	\$	15		21	1Ø91	916			1.86
12ØØ*	\$	1Ø		16	831	1203	3.4		.28
18ØØ	\$ /	ØВ		11	571.3	1750			2.78
24ØØ*	\$	8		8	415.5	2400			.28
36ØØ	\$	5		5	259.7	385			5.96
48ØØ*	\$	4		4	207.7	4813			.28
96øø*	\$	2		2	1Ø3.9	962			.28
192ØØ*	\$	1		1	51.9	1925			.28

<sup>\*</sup> DIP switch selectable

<sup>\*\*\*</sup>The quantum number zero is treated as 256 (\$100).



	COURT OF THE COURT	C151 68 C152 49 88 C154 00 01	C140 F0 08 C142 88 C143 D9 00 0 C146 D0 05 C146 09 E0 C14A 99 00 0 C14A 99 00 0	C142 88 C143 D9 QQ Q	C13E 08 C13C 30 OF C13E A4 35	C12B 9A F8 0 C12C AE F8 0 C12F 48 F8 0 C13C 50 03 C13S 90 43 C13S 90 78 0 C13A 48 C13B 08	C12C AE F8 C	C128 68 C129 68 C12A 28 C12B 94	C122 0A C123 BD FB 0 C126 AB C127 68	C11C 8D F8 0	C1112 48 C1112 AD FF C C1112 20 58 F C1118 BA C1119 BD 00 C	C10F 48 C110 98 C111 48	C109 08 C10A 78 C10B 86 35 C10D 48	C108 C107 18	C100 2C 58 F C103 70 04 C105 38 C106 90 FE	C100 C100 C100	C100 C100 C100	C100 C100 C100	C100 C100	C100 C100 C100	C100 C100 C100	C100 C100 C100	C100 C100 C100
--	--	-------------------------------------	---	-------------------------	-------------------------------------	---	--------------	--	---	--------------	---	-------------------------------	---	-----------------	---	----------------------	----------------------	----------------------	--------------	----------------------	----------------------	----------------------	----------------------

0 * * * * * * * * * * * * * * * * * * *		RINTER / REMOTE INPUT NT INTERFACE FIRMWARE JULY 21, 1978
0 * 0 * 0 * * * *	BY J (P	AMES R. HUSTON * 7-03, P8-01) * * * * * * * * * * * * * * * * * * *
0 * * * * 0 ; 0 ; 0 ; 0 ; 0 ; 0 ; 0 ;	PAGE EQUS *	
O * * * * *  O * * * * *  O * * * * *  O * * * *	EQU \$24 EQU \$28 EQU \$35 EQU \$35 EQU \$35 EQU \$36 EQU \$37 EQU \$37 EQU \$37 EQU \$37 EQU \$37 EQU \$37 EQU \$42 EQU \$42	CURSOR HORIZONTAL POSITION BASE SCREEN ADDRESS POINTER BIT COUNTER FOR THE PROPERTY OF THE PRO
0 ; 0 * GENER:	AL EQUATES *	
O * GENERA O FICK O STACK O INBUFF O KBD O KBDSTRB O DEV O ROMSW	EQU 195 EQU 100 EQU 1200 EQU 1000 EQU 10010 EQU 10080 EQU 10080 EQU 10080	CONTROL-U SYSTEM STACK BLOCK SYSTEM INPUT BUFFER KEYBOARD LIPUT KEYBOARD CLEAR DEVICE ACCESS DISABLES CO-RESIDENT \$CBOO ROMS
SLOT	VARIABLES *	
O BRATE O STPRITS O STATUS O COL O COL O BYTE O NO O FWDTH O FARTTY O NBITS O MSLOT O HIGH	EQU	THE BAUD QUANTUM NUMBER THE NUMBER OF STOP BITS THAN THE NUMBER OF STOP BITS THAN TO THE STOP THE STOP THAN TO THE STOP THAN TO THE STOP THAN THE STOP THE S
* MONITOR	SUBROUTINES	그 존경 보이 지원에 관하다 모네요.
CROUT COUT1 FRERR LORIS	EQU	DELAY SURROUTINE INCREMENTS AL AND COMPARES TO A2 OUTPUTS A CARRAGE RETURN CHARACTER OUT OUTPUTS "ERR" KNOWN "RTS" LOCATION
DFLTENTR DFLTENTR D SERIN D SEROUT D ENTRY	BIT IORTS BUS ENTRY SEC BCC * ORG *-1	SET THE V-FLAG (DEFAULT ENTRY) BRANCH ALWAYS SERIAL INPUT ENTRY
SEROUT	CLC CLV PHP	SERIAL OUTPUT ENTRY
	SEI SIX TEMPX FHA TXA FHA	SAVE INPUT RUFFER INDEX
	BITS JORTS SECT **-1 COLV FHHP STX COLV FHHP TEMPX TEM	SMITCH OUT ALL CO-RESIDENT \$C800 RC IDETERMINE SLOT ADDRESS RECOVER HIGH SLOT ADDRESS FROM STAC ICREATE \$NO FROM SLOT ADDR (\$CN)
	STA NO TAY FLA	PUT \$NO INTO REGISTER-Y ALSO
	PLA PLP TXS LDX MSLOT FHA	RECOVER CHARACTER RECOVER STATUS FLAGS RESTORE STACK POINTER PUT THE STACK POINTER SAVE THE CHARACTER
DODEF NORMIO SERINI	BUC NORMIO JSR DEFAULT BCC SEROUTI LDA OLDBYTE	SET ALL SLOT DEPENDANT LOCATIONS BRANCH IF NOT INPUT MODE SAVE FOR ESC TEST
GETIN DOCASE	PHA PHP BMI GETIN LDY TEMPX BEQ GETIN DEY CMP INBUFF,Y	IGNORE OLD BYTE IF UPPER CASE GET INPUT BUFFER INDEX VALUE, ON'T MODIFY BUFFER IF 1ST CHAR. OR IF OLDBYTE DOESN'T MATCH. WHAT'S IN THE INPUT BUFFER.
GETIN	BEG GETIN DEY CMP INBUFF,Y ENE GETIN ORA H\$ED STA INBUFF,Y JSR SHFTIN FLP	MAKE IT LOWER CASE PROPER!
	PLA EOR H19P BNE DOCASE	(OLD BYTE) IS IT "ESC" FALL INPUT FOLLOWING "ESC" MUST
DOCASE	PLA EOR H19R BNE DOCASE CLC LDA H1DF BCC CAPSONLY ORA FLAGS.X STA CASE	GO GET THE SERIAL INPUT RESTORE CARRY AND INTERUPT FLAGS (OLD RYTE). IS 11 "EST ALL INPUT FOLLOWING "ESC" MUST UPPER CASE OF UPPER CASE ALFA BRIT SO IF UPPER CASE ALFA BRANCH IF ATTER "ESC CASE IS ENABL IF BIT SO ITHEN LOVER CASE IS ENABL

1410 1410 1410 1410 1410 1410 1410 1410	CALLERY SET ON LINE FEED MODE  AND MATE  AND M	AND	Color   Colo	
Second Color   Seco	Second   S	STATE   COLUMN COUNT   COLUMN COUN	STOTE   STOT	SAC   CONTROL   CONTROL
A FLAGS.X  A ICARRY SET ON LINE FEED MODE  A MADD  ICHECK FOR CARRAGE RETURN  INCLUDED  ICHECK FOR CARRAGE RETURN  ICHECK FOR CARRAGE  ICHECK FOR COLLAX  ICHECK FOR CARRAGE  ICHECK FOR COLLAX  ICHECK FOR CARRAGE  ICHECK FOR CARRAGE  ICHECK FOR COLLAX  ICHECK F	A HAD ICARRY SET ON LINE FEED MODE  A HADD ICARCHER FOR CARRAGE RETURN  A COL, X  IFIX THE COLUMN COUNT  COOK  INCLUDED IT OUT  ICHECK OF CARRAGE	COLX   FIX THE COLUMN COUNT AUTO F MODE   COLU	Color	1
CARRY SET ON LINE FEED MODE PRECOVER CHARACTER PRECOVER CHARACTER PRECOVER CHARACTER PRECOVER CHARACTER PRECOVER CHARACTER PROBLEM PRO	CARRY SEI ON LINE FEED MODE IRECOVER CHARACTER ICHECK FOR CARRAGE RETURN  ISTANCH IF NOV AUDIT I BRANCH IF NOV AUDIT I BRANCH ALMAYS I CHECK VIDEO BIT I NO VIDEO BIT I HO VIDEO BIT I HE VIDEO IS ON IBRANCH IF CH>COL  MODIFY CH MODITAL IF APSOFT I OR MODIFY CHARACTER I TABLE CHECK I FERNACH IF NO TABLE I TABLE CHECK I FARMACH I COUNT CHECK I FRANCH I EN MODITAL I FARMACH I EN MODITAL I FAR	FIR   THE COLUMN COUNT   C840   C84		TAR CHECK   CRAD   CR
		CB 400 P 0 B 80 P 1 P 0 P 1 P	C841 85 34 3620 NOTINP C841 85 99 3640 NOTINP C841 64 99 3640 NOTOUT C847 67 99 3640 NOTOUT C848 85 38 3690 FARERR 3700 FARERR 37	CB35 95 36 CB36 95 36 CB41 00 99 36400 CB41 10 99 36400 CB45 DO F99 36700 CB47 AP U5 3B CB47 AP U5 3B CB48 BB
			34400 NOTOUT 34600 34600 PARERR 37000 GOODATA 371100 37700 * * * * * * * * * * * * * * * * * *	3410 3410 3420 NOTINP CCC GOODATA 3460 3460 3460 3460 3460 3460 3460 3460

COLUMN WIDTH Y TABLE TH OF STPBITS STATUS JT VECTOR OR. TIONS, PLEASE TY/STOP BIT OPTIONS ERO THEN PARITY, IF BIT ZERO IS PARITY TY IS EXPECTED OUNT ART BIT STOP BIT ... BIT RD WANTS ATTENTION START BIT THE STACK BEFORE RETURN LAY SUCH THAT DATA IS TAKEN OF THE SIGNAL (BRANCH ALWAYS) UND? AT DATA TIME 78 CYCLE TIMES TO CARRY Y NUMBER RATE 1) CYCLES ITY, THAT IS THE QUESTION T MATCH THE CALCULATED? FLAG IS ON GNAL IS HIGH BEFORE MAKE A WHOLE BYTE? BIT COUNT IN REGISTER-Y BHBACKETO BY ALLERNE. STERS

3DD 49 3D2 C9 3D4 90 3D6 49 3D9 4C	1F 02 F0	FD		4680 4690 4700 4710 4720 4730 4740	MXTOUT	ORI EOI BCOI EOI FLI	A #\$80 R #\$E0 P #\$1F C MXTOUT R #\$E0	
DE BD	28	05		4740 4750 4760 4770	INF INSH			REPLACE FLASHING CURSOR
1E3 C9	95 02			4780 4790 4800		STA ORA CMF	(BASL),Y H\$80 HFICK NOTFICK (BASL),Y H\$51 CASE	SCREEN PICK (CONTROL-U)?
E 9 C 9	28 E0			4810 4820	NOTFICK	LDA	(BASL),Y	GET SCREEN CHARACTER DO NOTHING TO UPPER AN NON-ALFA
IF 1 90	35 E0			4840 4850		CMF BCC AND CMF	CASE H\$ED	MAKE LOWER TO UPPER IF LC NOT ENABLED
F1 90 F3 A4 F5 C0 F7 F0 F9 29	E0 08 37 F0 03 71			4860 4870 4880		P.CC LDY CPY	EXIT1 CSWH	IF VIDEO OUT ONLY, THEN CHAR SHOULD BE DISPLAYED INVERSE.
IF A AC	7F 58	FF		4890 4900 4910		BEG	HATE	OTHERWISE JUST STRIP BIT 7 (DUMMY LDY ABSOLUTE)
FC 29	1F 78	40		4920	IVERSE EXITI	LDY OND STAP PCSP	*-2 #\$1F	MAKE IT INVERSE HIDEO
01 28 02 80 04 C9 06 30 08 00 00 A9 0C 10	15 CC 09			4940 4950 4960 4970	EXITI	PLP		MAKE IT INVERSE VIDEO SAVE FOR NEXT INPUT TO CORRECT BUFFER IF CARRY CLR, DO "ESC" FUNCTION
06 30 09 00	DE			4970 4980 4990			NOTESC H\$CC UPPER NOTESC H\$20 FLAGS,X STORFLGS H\$DF FLAGS,X FLAGS,X	IS IT AN ASCII "L" CAPSLOCK COMMAND IF >L
DC 1D OF DO	38 05	07		5000		BNE LDA ORA BNE	#\$20 FLAGS,X	ENABLE LOWER CASE
0F 00 11 A9 13 30 16 90	05 0F 38	07		5020 5030 5040	UPPER	LDA	STORFLGS #\$DF	BRANCH ALWAYS TAKEN DISABLE LOWER CASE
19 18 14 80 10 09	38	07		5050 5060 5070	STORFLGS	STA	FLAGS, X	INDICATE INPUT RETURN
01 28 02 80 02 6 30 08 00 00 00 00 00 00 10 00 11 11 Ay 113 30 119 18 114 80 115 90 122 AC	88 89 58	04	- (4)	5080		STA CLC LDA OFA STA LDY	STATUS,X #\$80 STATUS,X IORTS *-2 CH	INDICATE INPUT RETURN INDICATE AFTER INPUT (TO OUTPUT)
25 84	24			5100 5110 5120 5130	EXIT2	ORG	IORTS *-2	(ANOTHER DUMMY)
23 84 25 68 26 A8 27 68				5130 5140 5150		TAY	LH	RESTORE CURSOR HORIZONTAL
28 AA 29 68 24 80	na			5170		FLA		RESTORE REGISTERS
2C AD	03 78	06		5180 5190 5200	RETOUT	PCS	RETOUT OLDBYTE	; EXCEPT REG. A IF INPUT RETURN FRETURN NEW INPUT
30 60				5210 5230 5230		PLF RTS FAG		RETURN TO CALLER
31				5250	* BAUD	TAE		
1 PO				5260 5270 5280	ÉTAB			1 110 BAUD
31 80 32 90 33 40 34 10 35 08 36 04				5290		DFB DFB DFB DFB	\$ 80 \$ 90 \$ 40 \$ 10 \$ 8 \$ 42 \$ 1	: 110 BAUD : 134-5 BAUD : 300 BAUD : 1200 BAUD : 2400 BAUD : 4400 BAUD : 9400 BAUD : 19400 BAUD
6 04				5320		DF B DF B	18	F 1200 BAUD F 2400 BAUD F 4800 BAUD F 9600 BAUD
9 01				5340	WIDTH	DFB DFB	ši .	: 110 BAUD : 134.5 BAUD : 300 BAUD : 1200 BAUD : 2400 BAUD : 4800 BAUD : 9600 BAUD : 19200 BAUD
8 4 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8				5360 5370 5380	WDTAB TOTAL	DFB DFB DFB DFB FAGE	\$29 \$48	: 49 COLUMNS
C 84				5390 5390 5400 5410 5420		DF B.	\$50 \$84	40 COLUMNS F72 COLUMNS F80 COLUMNS F132 COLUMNS
				54460 54460 555544780 555555555555555555555555555555555555	THESE OUT), L ADDRESS RAM ADD HAVE BE FRIOR T ANYTHIN ON EXI	ARE OCAN	THE BATCH R IONS \$3C & IONS \$1C & IONS \$1C & IOCATIONS FOR THE BA NITIALIZED TRY. ON EN GISTERS ARE	OUTINES. ON EITHER ENTRY (IN OR 130 MISS. CONTAIN THE PEGINNING RAM 130 MISS. CONTAIN THE FEGUNING RAM 150 MISS. THE CARD MUST AND PARTLY AFFECTS WHITE TO SET TRY A.X.2Y REBISTERS MAY CONTAIN UNDEFINED.
90 F	B F	F		5560 1 5570	NPUT	BIT	IORTS	SET VFLAG TO INDICATE INPUT
20 9 8 8 F B B B B B B B B B B B B B B B B B	8 0	74		5580 5590 5600 5610 5620 5630	TUATU	OCLULLLA LESSELES JUNE LE LES LES LES LES LES LES LES LES LES	*-1 MSLOT STATUS,X A A	GET READY FOR BATCH MOVE
44				5640	ATCH	LSR	Ä	DETERMINE CHECKSUM
AE FB BB 444 448 448 448 448 448 448 448 448 448	02300			5660 5670 5680 5690	ATCH	LDY	H+O CKSUM COUNT MOVIN (AIL),Y BYIE,X CKSUM CKSUM CKSUM FINISH COUNT MOVOLIT	FINITIALIZE CHECKSUM AND COUNT
90 B	8 0 C	5		5710 M 5710	00001	STA	BYTE,X	GET THE DATA TO BE OUTPUT
2U B	A C	9		5700 M 5710 5710 5730 5730 5740 5750 5760 5780 5780 5780 5780		STA	CKSUM	;UPDATE CHECKSUM ;GO OUTPUT IT
80 11 E6 43 00 E8	1 3			5760 5770		P.CS INC	FINISH COUNT	GO OUTPUT IT SINCREMENT POINTERS
BO 43 BO 48 BO 48 BO 48 PO D 7 PO 08 BO 08				5780 5790		PLP		DO CHECKSUM IF CARRY CLR
90 B6 20 AA 90 D5 68 D7 20 BE 20 20 38	3 05			5810 5820		LDA STA JSR	BATCH CKSUM BYTE,X SHOUT	
20 AA 90 D5				5830 5840 5850 F		BICC	BATCH	GET THE CHECKSUM FOR THE LAST PLOCK OUTFUT FOUTFUT THE CHECKSUM PRANCH ALWAYS
80 07 20 8E 20 2D	FE			5810 5820 5830 5840 5850 F 5860 5870 5880		PCS JSR JSR	RETBATCH CROUT PREER	
20 BE	tr							

COPPE D AB 0 AB 0 COPPE D AB 0 AB 0 COPPE D AB 0 AB 0 COPPE D AB 0 COP	78 N7	0024 6701 6771 6773 6773 6773 6773 6773 6773 677		PAROUT  C748  C3885  C1020  C1974  C1	STOP BITS ARE SENT: INOW Y**NO**(STOP BIT OR PARITY BIT) ITHEN SET THE ZERO FLAG  BATCH C.	
C9E9 C9EB C9ED C9EC C9FO C9FO C9FO C9FO C9FO C9FO C9FO C9F	35 0E 35 88 89 04	658 650 661 662 663 664 665	STOP BITS		PRANCH ALMAYS TAKEN TITHING DIRTHO DOTA TITHING DISTON DOTA TYPES. DISTON TO DOTA THE BUSING DOTA THE BUSING DOTA TO BUT WHAT ABOUT STOP BITS? TCARRY CLEAR, THEN DONE  IF SELECTED) YET TO DO THUT THE NUMBER OF STOPBITS IN COUNTER THOM TO SEE IF A PARTIY BIT IS TO BE SEN THE SENT, ELSE JUST	
00000000000000000000000000000000000000	07 09	645 647 648 649 650 651 655 655 655 655	AND SECOND SECON	PARITY PARITY	JOET THE BAUD NUMBER  JDELAY = 53 * (BAUD NUMBER-1) CYCLE TIME	
CC9884 6 3 3 5 5 6 6 7 9 8 6 7 9 8 6 7 9 8 9 9 8 6 7 9	BB 06 35 01 PB 04 FB 06 78 07 00 BB 05 BB 05	633 6335 6337 6337 6339 642 642 642	0 FHF 0 LD7 0 ST) 0 ST) 0 ST) 0 ANN 0 OR 0 OR 0 ST 0 LD7 0 ANN 0 OR	NEITS,X NCOUNT H\$1 STATUS,X NO PARITY H\$0 BYTE,X AND	GET THE NUMBER OF BITS/WORD STORE IT IN THE BIT COUNTER FOR EVEN OR ODD PARITY INITIALIZE PARITY SHIFT FOR START BIT BIT O OF BYTE -> BIT O OF ACCUMULATOR	
0.000 0.000		0	0 : * * * * * * * * * * * * * * * * * *		UTPUT ROUTINE Y CAN BENNYY X = CN  ) REGS A AND Y CAN BENNYY X = CN  MUST BE IN BYTE (+ CN); IT DATA IS  15 (5). THE LEFTOVER HIGH BITS MUST  FOR 7 BITS: BYTE = IXXXXXXX)  MULUDENG THE YEART BIT) MUST BE IN  KOLUDENG THE YEART BIT) MUST BE IN  KOF STOPE THE YEART BIT IN  MUST BE IN NO (+ CN); IT IN  AND Y = 0. F - STATUS IS ADDIFIED  ; SEI FOR NO HARD INTERRUPTIONS	
C998 C998 C9983 C9984 C9984 C9987 C9988 C988	0 40 C8 0 88 05 5 42 8 A5	603 608 608 608 608 610 611 611	0 BCS 0 JSR 0 CMF 0 CMF 0 CMF 0 BE 0 BE 0 ERR1 CL	SHETIN BYTE,X CKSUM BATCH C FINISH	PRANCH IF NO CHECKSUM GET THE CHECKSUM INPUT  INDICATE CHECKSUM MODE  INDICATE ERROR CONDITION BRANCH ALWAYS TAKEN	
C 9 8 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	0 PB 05 1 30 2 42 6 42 7 FC 1 E23 0 FS	591 593 594 595 596 597 598 600 601 602 603	0 MOVIN JSE 0 PMI 0 LDA 0 EOE 0 STA 0 STA 0 JSE 0 BCS 0 EOE	ERRI EYTE, X E (ALL), Y CKSUM NXTAI FINISH COUNT MOUTN	IGET THE INPUT AWAY  IUFOATE THE CHECKSUM I INCKEMENT RAM FOINTER  IUFOATE THE BYTE COUNT IERANCH IF BLOCK NOT FILLED	